

Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Error Codes
1	BRS L1	1243	("chip-substrate" or chip adj2 substrate) near3 connect\$4	USPA T	2001/10/1 5 20:37			0
2	BRS L2	2	gold and (tin same hypereutectic)	USPA T	2001/10/1 5 20:41			0
3	BRS L3	4	("4914814" "5154341" "5172852" "5180097") .PN.	USPA T; JPO	2001/10/1 5 20:20			0
4	BRS L6	0	1 and 2	USPA T	2001/10/1 5 20:22			0
5	BRS L7	593	gold and 1	USPA T	2001/10/1 5 20:22			0
6	BRS L8	0	hypereutectic and 7	USPA T	2001/10/1 5 20:23			0
7	BRS L9	263	hypereutectic	USPA T	2001/10/1 5 20:31			0
8	BRS L10	6	(above adj2 eutectic or hypereutectic) and 1	USPA T	2001/10/1 5 20:38			0
9	BRS L11	0	9 and 1	USPA T	2001/10/1 5 20:37			0
10	BRS L12	57	("chip-substrate" or chip adj2 substrate) near3 connect\$4	US-P GPU B	2001/10/1 5 20:39			0
11	BRS L13	1	(above adj2 eutectic or hypereutectic) and 12	US-P GPU B	2001/10/1 5 20:39			0
12	BRS L14	91	("chip-substrate" or chip adj2 substrate) near3 connect\$4	EPO	2001/10/1 5 20:52			0
13	BRS L15	0	(above adj2 eutectic or hypereutectic) and 14	EPO	2001/10/1 5 20:39			0
14	BRS L16	107	above adj2 eutectic or hypereutectic	EPO	2001/10/1 5 20:52			0
15	BRS L17	9	sold\$5 and 16	EPO	2001/10/1 5 20:40			0

Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Error ID's
16 BRS	L118	0	gold and tin and 17	EPO	2001/10/1 5 20:42			0
17 BRS	L119	0	Au and Sn and 17	EPO	2001/10/1 5 20:42			0
18 BRS	L120	177	above adj2 eutectic or hypereutectic	JPO	2001/10/1 5 20:53			0
19 BRS	L21	285	("chip-substrate" or chip adj2 substrate) near3 connect\$4	JPO	2001/10/1 5 20:53			0
20 BRS	L122	0	20 and 21	JPO	2001/10/1 5 20:53			0
21 BRS	L23	517	above adj2 eutectic or hypereutectic	DERW ENT	2001/10/1 5 20:53			0
22 BRS	L24	548	("chip-substrate" or chip adj2 substrate) near3 connect\$4	DERW ENT	2001/10/1 5 20:53			0
23 BRS	L25	0	23 and 24	DERW ENT	2001/10/1 5 20:53			0